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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/633,993	08/04/2003	Jaroslav Hynecek	TI-36483	5016
23494	7590	11/29/2005	EXAMINER	
TEXAS INSTRUMENTS INCORPORATED P O BOX 655474, M/S 3999 DALLAS, TX 75265			MONDT, JOHANNES P	
			ART UNIT	PAPER NUMBER
			3663	
DATE MAILED: 11/29/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action Before the Filing of an Appeal Brief</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/633,993	HYNECEK, JAROSLAV
<b>Examiner</b>	<b>Art Unit</b>	
Johannes P. Mondt	3663	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 18 November 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1.  The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.  
b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### NOTICE OF APPEAL

2.  The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

#### AMENDMENTS

3.  The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because  
(a)  They raise new issues that would require further consideration and/or search (see NOTE below);  
(b)  They raise the issue of new matter (see NOTE below);  
(c)  They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or  
(d)  They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_. (See 37 CFR 1.116 and 41.33(a)).

4.  The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.

6.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7.  For purposes of appeal, the proposed amendment(s): a)  will not be entered, or b)  will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: \_\_\_\_\_.

Claim(s) rejected: 11-17.

Claim(s) withdrawn from consideration: 1-10 and 18-20.

#### AFFIDAVIT OR OTHER EVIDENCE

8.  The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9.  The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10.  The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

#### REQUEST FOR RECONSIDERATION/OTHER

11.  The request for reconsideration has been considered but does NOT place the application in condition for allowance because:  
See Continuation Sheet.

12.  Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). \_\_\_\_\_

13.  Other: \_\_\_\_\_.

  
**JACK KEITH**  
**SUPERVISORY PATENT EXAMINER**

Continuation of 11. does NOT place the application in condition for allowance because: Traverse of the rejection of claim 11 over Hynecek is not persuasive because (a) "bipolar device" is not claimed by Applicant nor is this comment relevant at all to the issues at hand in any other way, (b) the "virtual" gates are in fact clocked gates (see first paragraph of "Background of the Invention", the adjective "virtual" pertaining to the replacement of one silicon level by a P+ junction and does not at all imply that the property of being clocked is virtual: the virtual gates in Hynecek are indeed the MOS gates that are being clocked (see again first paragraph of "Background of the Invention")); and (c) Applicant's field plates 305 (page 10 of specification) and semiconductor regions 40/42/44/46 do not patently distinguish because both are semiconductor regions biased through wiring to a bias terminal (see page 10 of the specification and col. 2, l. 42-59 in Hynecek). Furthermore, the traverse of the rejections of claims 11-17 based on Bosiers et al as primary reference is not persuasive, because field plate 38 surrounds on both sides the first clocked gate 39, hence the rejection is in line at least with meaning d of "surrounding" as given in Merriam-Webster's Collegiate Dictionary, 10th Edition.